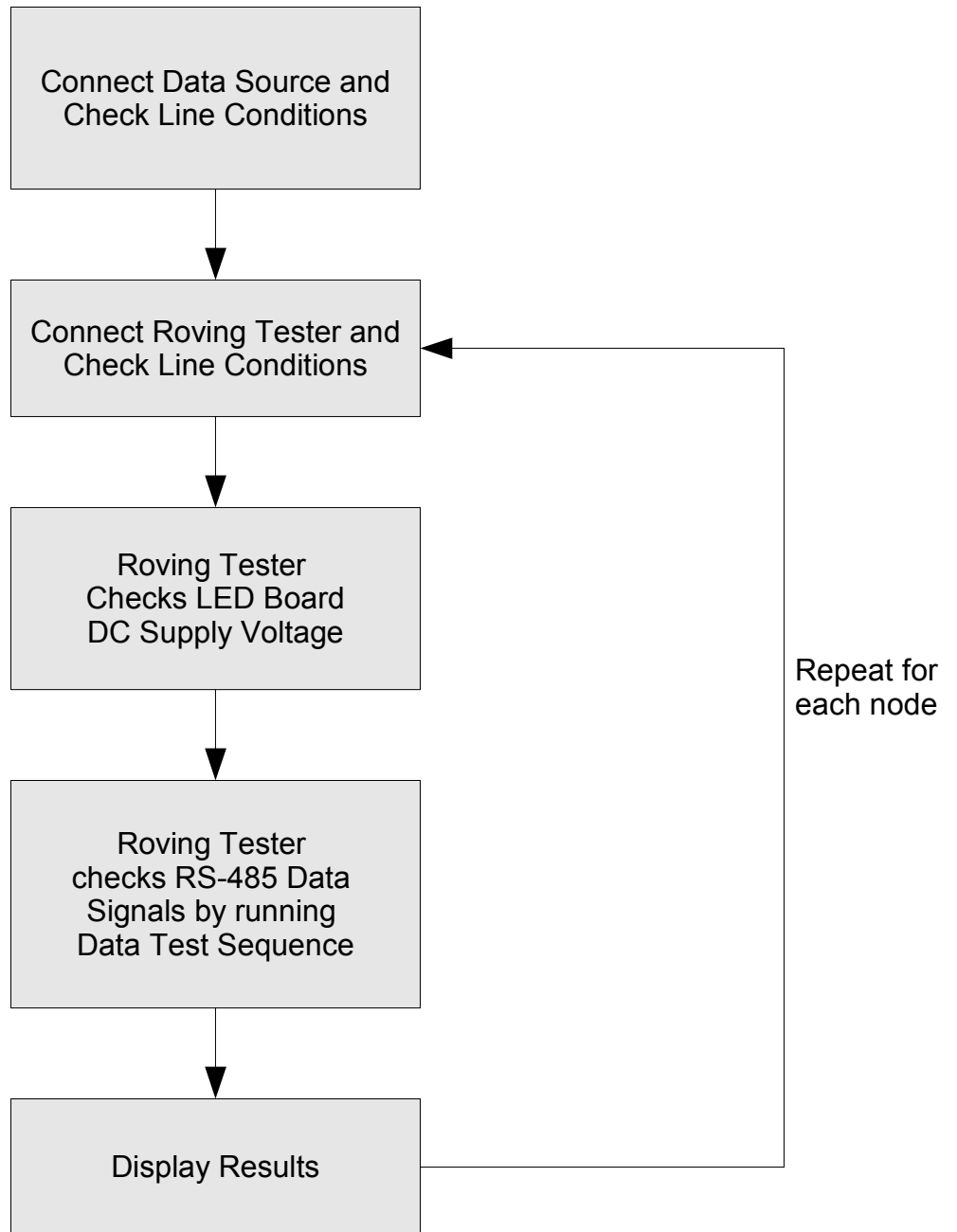
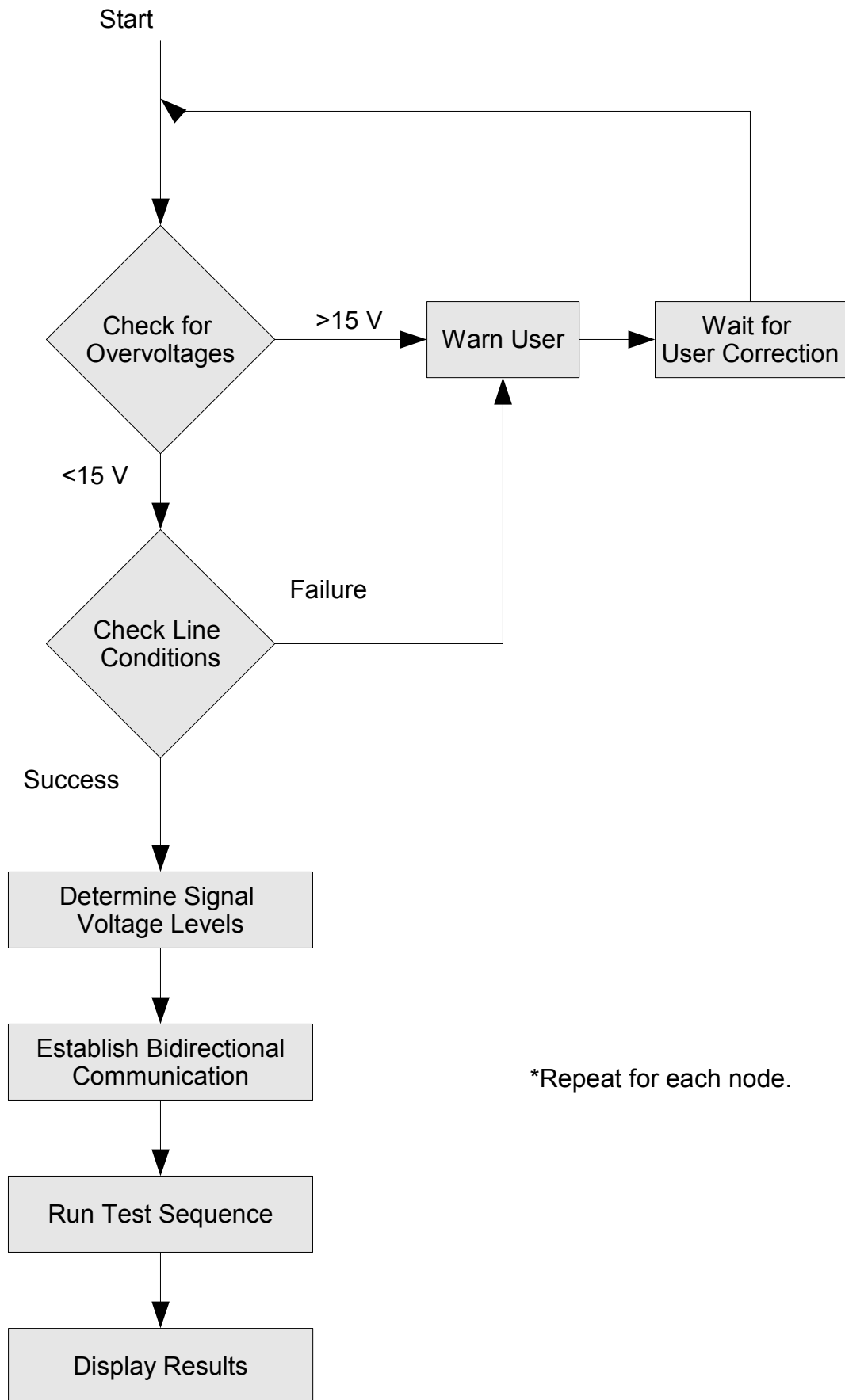


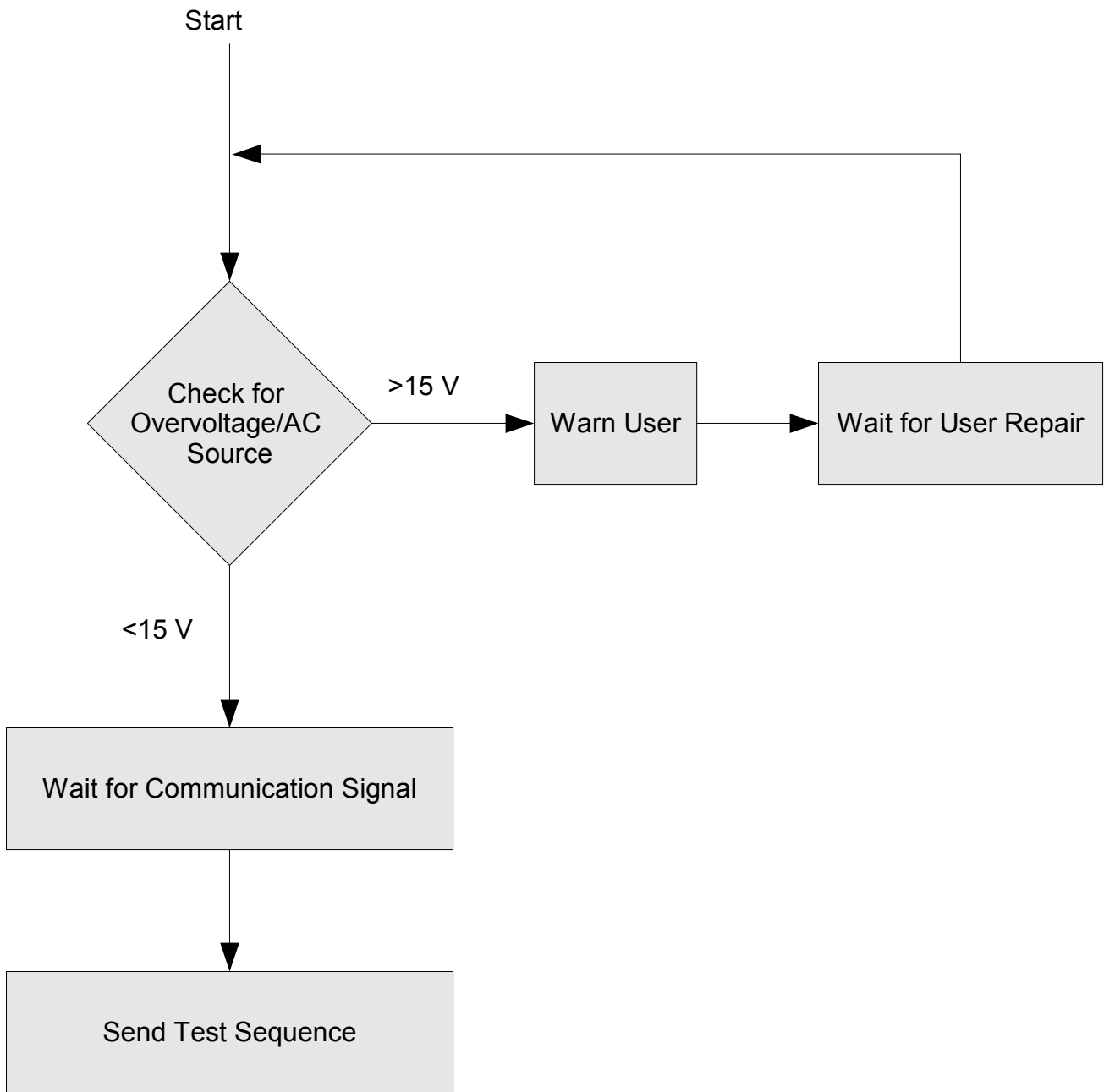
# Master Functional Block Diagram



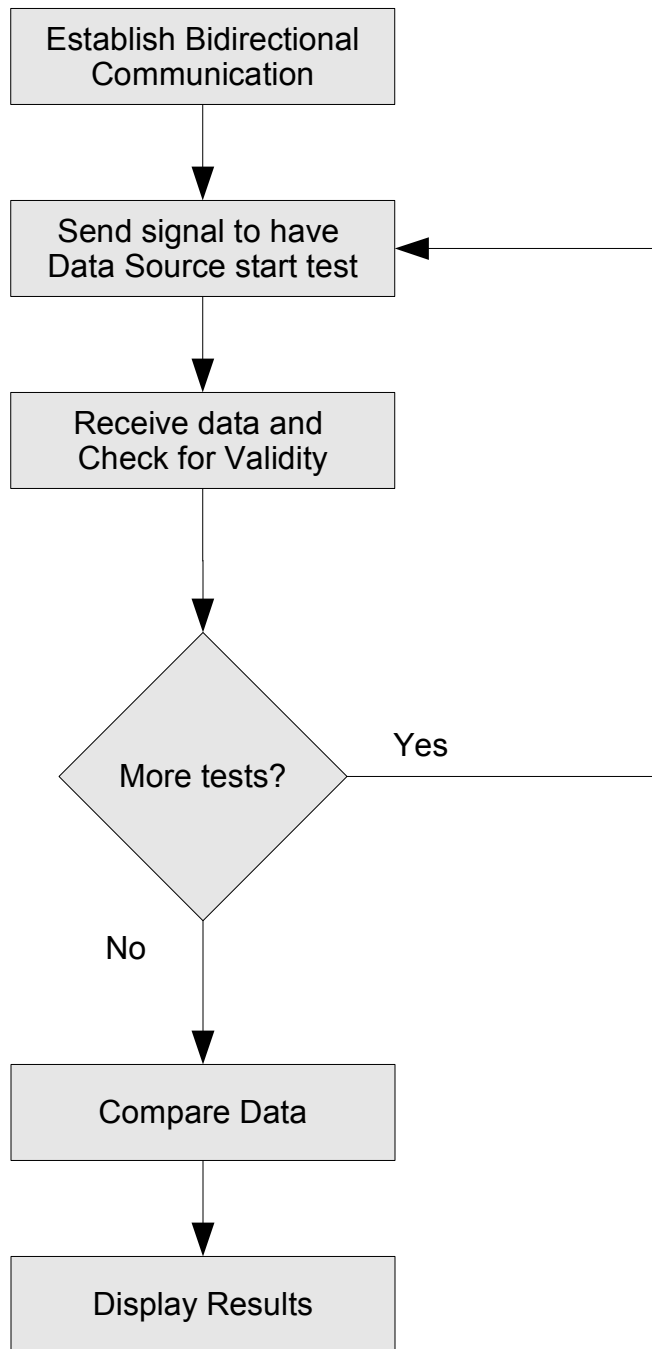
# Roving Tester Functional Block Diagram



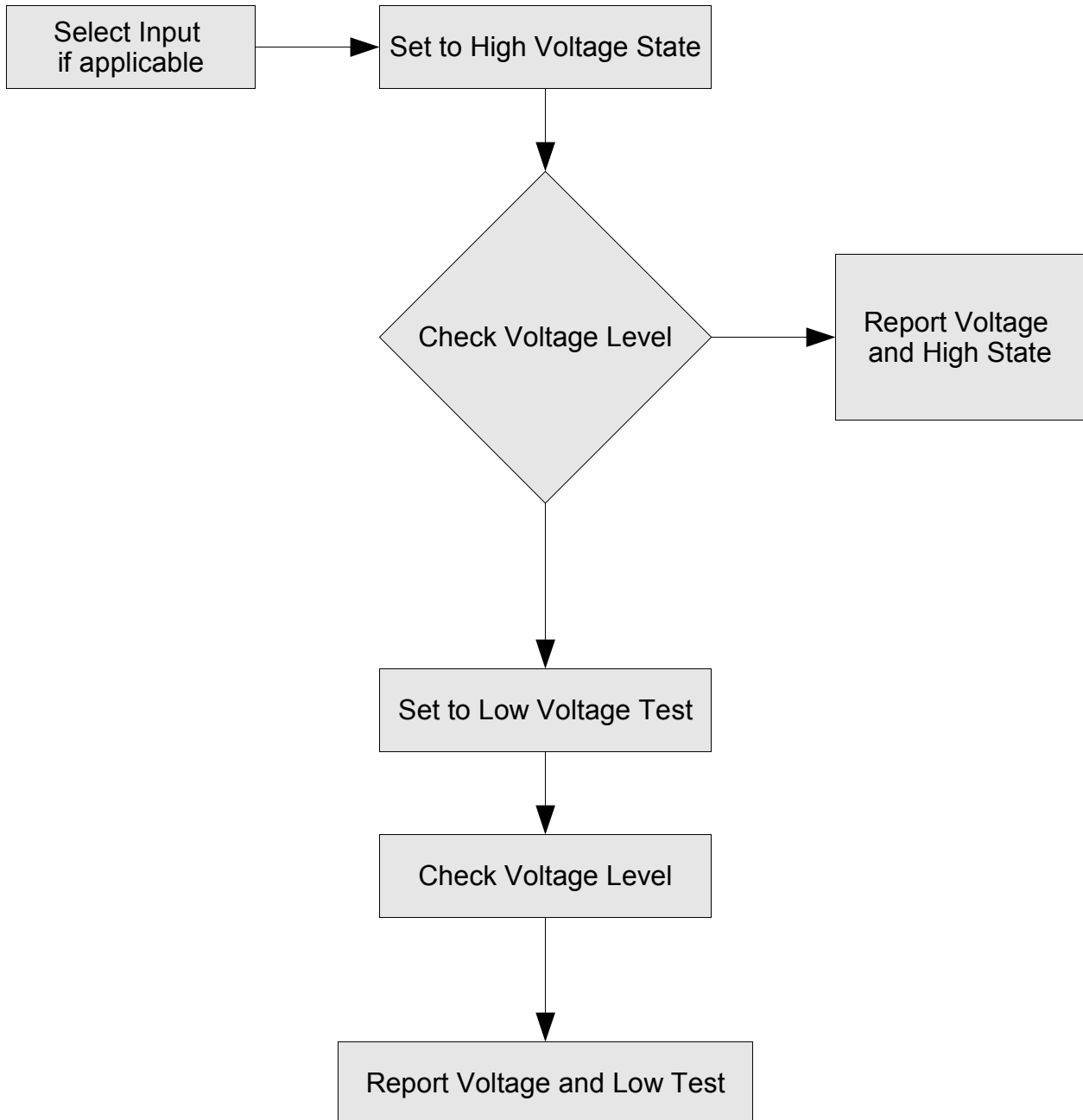
# Data Source Functional Block Diagram



# Test Sequence Functional Block Diagram



# Check Voltage Level Block Diagram



# Transmit Block Diagram

