CHAPTER 7

M68HC12 PARALLEL I/O

OBJECTIVES

This chapter describes the parallel I/O capabilities of the M68HC12. You will find that almost all I/O features must be programmed or initialized before use by setting and resetting bits in control registers. Examples show how this is done.

This chapter has covered the parallel I/O capabilities of the M68HC12 microcontroller. The summary points are:

- The MC68HC812A4 and MC68HC912B32 versions have differing I/O capabilities.
- When ports are not being used for expanded mode or other I/O they may be used for general purpose parallel I/O.
- Ports A, B, C, and sometimes D may not be used for I/O when in expanded modes.
- All I/O ports have programmable functions.
- Bidirectional ports have Data Direction Registers to specify the data flow direction.
- Port S shares functions with parallel I/O and the serial I/O.
- Port AD shares functions with parallel I/O and the A/D converter system.