

Errors in Software and Hardware Engineering: Motorola M68HC11

January 13, 2003

Pg	Error Correction	Thanks
12	Example 2-4: The comments for the indexed addressing examples should have parenthesis around the source operand to show indexed addressing: <pre>LDAA 0,X (ACCA) <- ((IX)+0) LDAA 64,X (ACCA) <- ((IX)+64) LDD 64,X (ACCD) <- ((IX)+64):((IX)+65)</pre>	John Glover
17	The discussion on Example 3.1 states that <i>line 0013 locates</i> locates the program. It should read <i>line 0012 locates</i> the program.	Jaime K. Mani
39	Line 0005 in Example 4-1 should be: <pre>LDAB \$64 (B) <- (\$0064)</pre>	
58	Second to last sentence should read: Example 4-32 shows how to use the ABY instruction to linearize a value input from the M68HC11 A/D converter.	Ronald J. Spencer, Jr.
63	Example 4-36: The example 0.375/0.5 should show the dividend to be .0110 , not .0100. <pre>.0110 .1100 ----- : ----- .1000 1000)0110.0000</pre>	Eric Durant
66	Table 4-9. The symbolic operation for the COMA instruction should show the A register being complemented, not the B register.	Jack Borgeson
82	Example 5-2 should read: <pre>BF D000 D02F FF</pre> instead of <pre>BF D000 D02 FF</pre>	
99	In Table 6-4, the instruction on line 0013 should be: bls ELSE_PART ; Must use bls for unsigned data	Ji Chen
105	Example 6-6. Line 0029 should be <code>lds #STACK</code>	Tony Dreher
111	Problem 6.6. The WHILE statement should be <code>WHILE K1 < K2</code> .	
171	The PPROG register should be equated: <pre>PPROG EQU \$103B</pre> not \$1028	Pam Yakymyshyn
179	The bits for W1A15-W1A13 base address should show: <pre>Window Size Valid Boundary 8K Any 8K boundary \$0000, \$2000, \$4000,... 16K Any 16K boundary \$0000, \$4000, \$8000,... 32K May be \$000, \$8000</pre>	
295	In problem 6.5, line 002 should read: <pre>0002 0000 b60012 ldaa K1</pre>	Femi Babarinde
296	Answer to problem 6.7 should be K1=2, K2=-2, K3=-2.	Pam Yakymyshyn

Please let me know if you find other errors. Please email me at fcady@ee.montana.edu. Thanks.